

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 1.53(b) Divisional Application of:

ANEZAKI, Toru et al.

Group Art Unit: **2813 (prior)**

Serial No.: **Div. of 10/091,289**

Examiner: **Nguyen, T. (prior)**

Filed: **July 25, 2003**

For: **SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 25, 2003

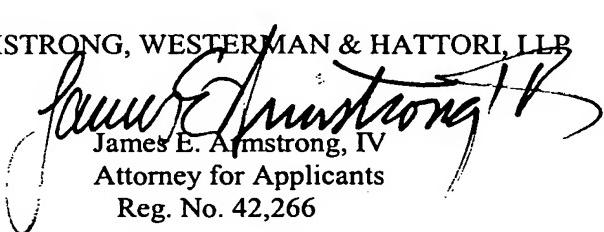
Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/696,945.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosure: PTO-1449

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| INFORMATION DISCLOSURE CITATION PTO-1449 | Atty. Docket No. 001443B | Serial No. Div. of 10/091,289 |
| Applicant(s): ANEZAKI, Toru et al. | | |
| | Filing Date: July 25, 2003 | Group Art Unit: 2813 |

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Examiner

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